

DS Tested

; YbYfU` 8YgWf]dh]cb`

Split gate trench MOSFET technology
 Excellent package for heat dissipation
 High density cell design for low R_{DS(ON)}
 Moisture Sensitivity Level 1
 Epoxy Meets UL 94 V-0 Flammability Rating
 Halogen Free
 Part no. with suffix "Q" means AEC-Q101 qualified

5dd`]WUh]cbg`

Power switching application
 Uninterruptible power supply
 DC-DC convertor

5Vgc` ihY`AUI]a i a`FUh]b]g` (T_J=25 unless otherwise noted)

DUfU a YhYf`	Gma Vc`	@]a]h`	I b]h
7	H ₅ 1 %\$\$	13	

		g=10V, R _G =25 , L=0.5mH, IAS=23A	EAS	132.25	mJ
Total Power Dissipation (Note 1,2)	Steady-State	T _A =25	P _D	3.7	W
		T _A =100		1.8	
Total Power Dissipation (Note 1,3)	Steady-State	T _C =25		93	
		T _C =100		46	
Junction and Storage Temperature Range			T _J , T _{STG}	-55 +175	

H\Yf a U`fYg]ghUbWY

DUfU a YhYf`	Gma Vc`	Hmd`
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		A=B=A I A` D57?5 ; 9fidWgI`	-BB9F`6CL` E I 5BH=HMfidWgI`	C I H9F`7 5FHCB` E I 5BH=HMfidWgI`	89@=J9FM` AC89`
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M>6)8\$; \$* <E'

9`YWhf]WU`7 \UfUWhYf]gh]Wg' (T_J=25 unless otherwise noted)

DUfU a YhYf'	Gm a Vc''	7cbX]h]cbg'	A]b'	Hmd'	AUI'	I b]hg'
GhUh]W'DUfU a YhYf'						

M>6)8\$;\$*<E'

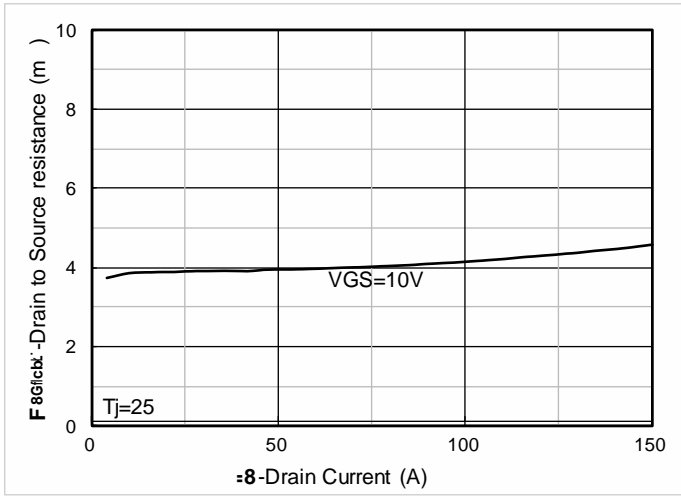


Figure 7: RDS(on) VS Drain Current

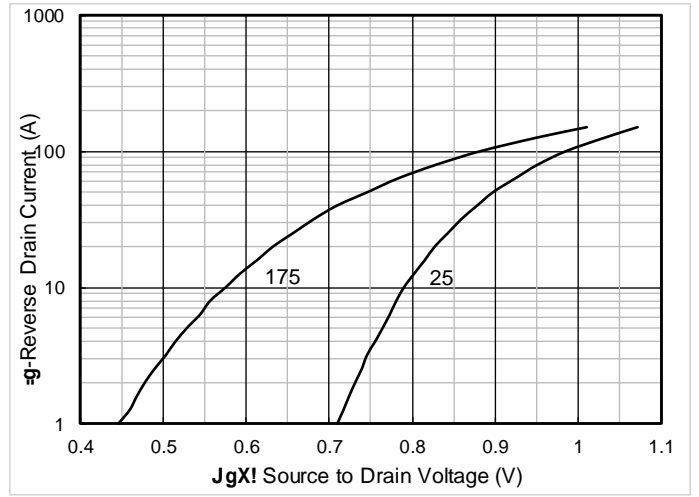


Figure 8: Forward characteristics of reverse diode

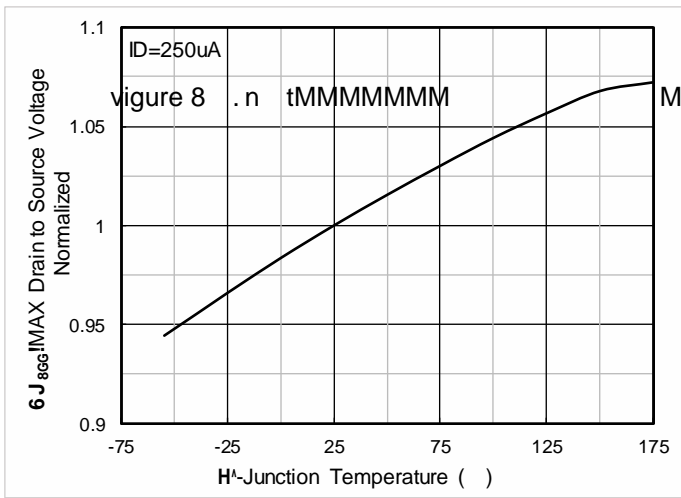
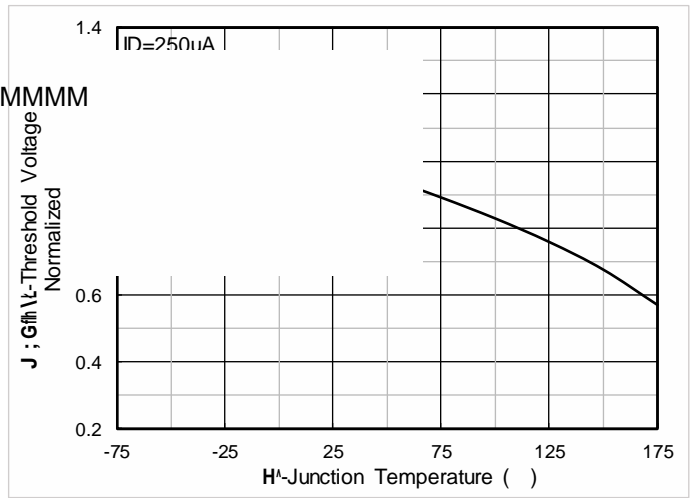
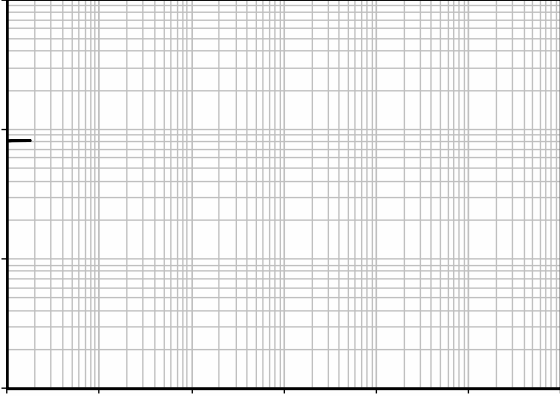


Figure 8 . n tMMMMMMMM

MMMMMMMM







HC!&* '!<M'DUW_U[Y']bZcf a Uh]cb

SYMBOL	MIN.	
	A2	0.000
b2		0.050
c		
c2		
D2		
E		
E1		

'LVFODLPHU

7KH LQIRUPDWLRQ SUHVHQWHG LQ WKLV <DQJ> KRXVH QDWLRQV MIRHU (WHIHFWURQ HFRQRJH QRO
ULJKW WR PDNH FKDQJH RU ZWLVK RXSWHQR SDURVGLXFWV RIGWVSOD\HG KHUHLQ WRGLHPSLWRY
RWKHU ZLVH

7KH SURGXFW OLVWJH GHWHULREH LXV BSHZLWHKFDXWR RERWIDYRUQRWHGLQVPHGH FDO OLIH
VDYLQJ OLIHVXVWD <DQJ> R UODLQD QDRQXPMV ERKU DLSREQOLELQRW\ DQWGPDUH
VXFK LPSURSHU XVH RI VDOH

7KLV SXEOLFDFWLRQ SXOSSHUW DGDVLQURQPDVSLRQLSHGHYRURXDGGLRQLRSDODVLRWHUPLWWRSX
ZZZ \DQJMLHRFRPRQVXOW \RXUH QH DDFHW R DQJWKWD OFH