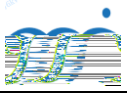


**Maximum Ratings** ( $T_a=25$  Unless otherwise specified)

Item	Symbol	Unit	Conditions	Value
Device marking code				TL
Collector-base voltage	$V_{CC}$	V		50

+6 collector-emi5R+6 ni emiR+6

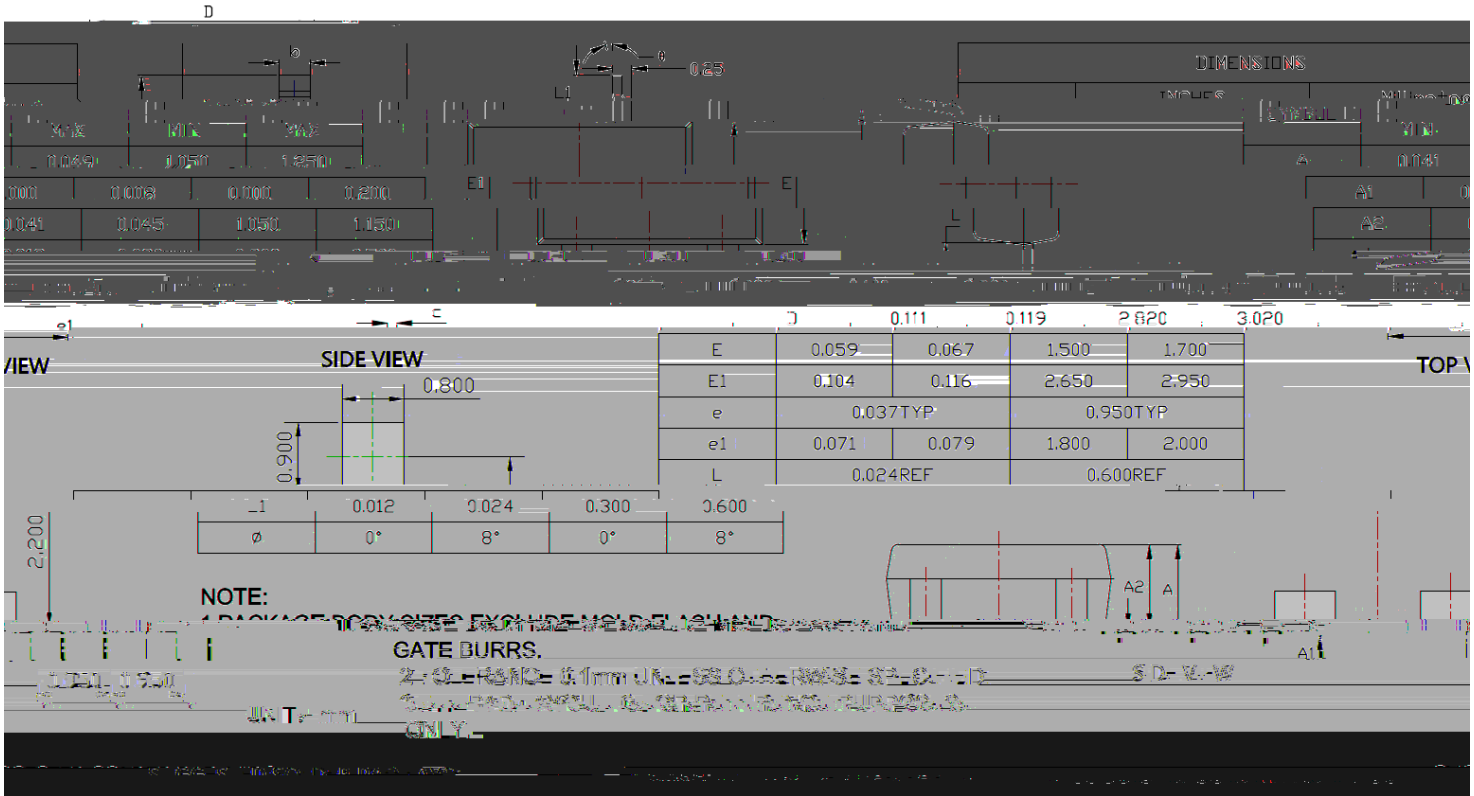


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**Electrical Characteristics** ( $T_a=25$  Unless otherwise specified)

Item	Symbol	Unit	Conditions	MinDC
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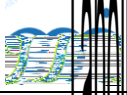
**Note:**

1. All dimensions are in millimeters (mm) unless otherwise specified.  
 [所有尺寸均以毫米为单位, 除非另有说明]
2. General tolerances:  $\pm 0.10\text{mm}$  unless otherwise specified.  
 [通用公差为  $\pm 0.10\text{mm}$ , 除非另有说明]

gate vestiges shall not exceed 0.1mm in maximum.  
 [尺寸和公差均指不包括最大0.1mm的毛刺和浇口残留部分]

4. All dimensions shown are exclusive of burrs and gate residues. Burrs and gate residues shall not exceed 0.15mm in maximum.  
 [所有尺寸均不包括毛刺和浇口残留, 毛刺和浇口残留的尺寸最大不得超过0.15mm]

Dimensions D and E use the overall external dimensions excluding the mold flash, lead flash, creeps and burrs, but include the maximum gate vestiges.  
 [尺寸b不包括单边最大0.100 mm的中筋凸出部分]



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## Disclaimer

The information presented in this document is for reference only. Yangzhou Ya41(i)doj(n)5(l)-eer