



- Adopt FRED chip
- Low forward Voltage drop
- Fast reverse recovery time
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability

Typical applicat: Tin plated leads, solderable per J-STD-001 and JESD22-B102
: As marked

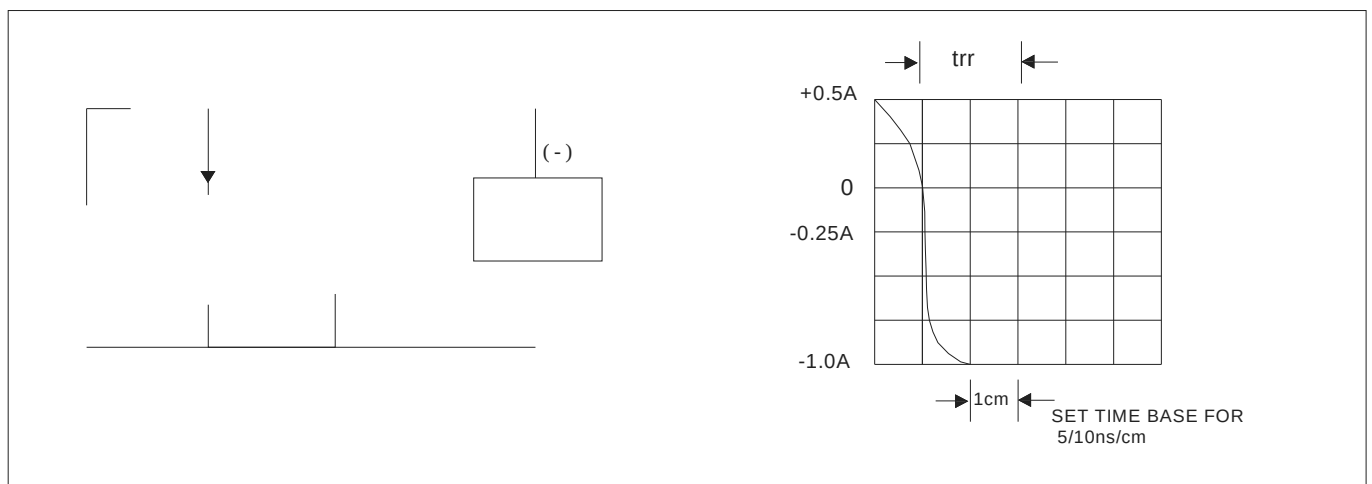
■ (T_j=25 Unless otherwise specified

Device marking code			MUR1040CT
Repetitive Peak Reverse Voltage	V _{RRM}	V	400
Average Rectified Output Current @60Hz sine wave, R-load, T _c (FIG.1) Total	I _O	A	10
Surge(Non-repetitive)Forward Current @60Hz half sine-wave, 1 cycle, T _j =25 , Per leg	I _{FSM}	A	50
Current Squared Time @1ms≤t≤8.3ms T _j =25 Per leg	I ² t	A ² s	10
Storage Temperature	T _{stg}		-55 ~ +175
Junction Temperature	T		
Typical Junction capacitance @4V,1MHz	C _j	pF	50



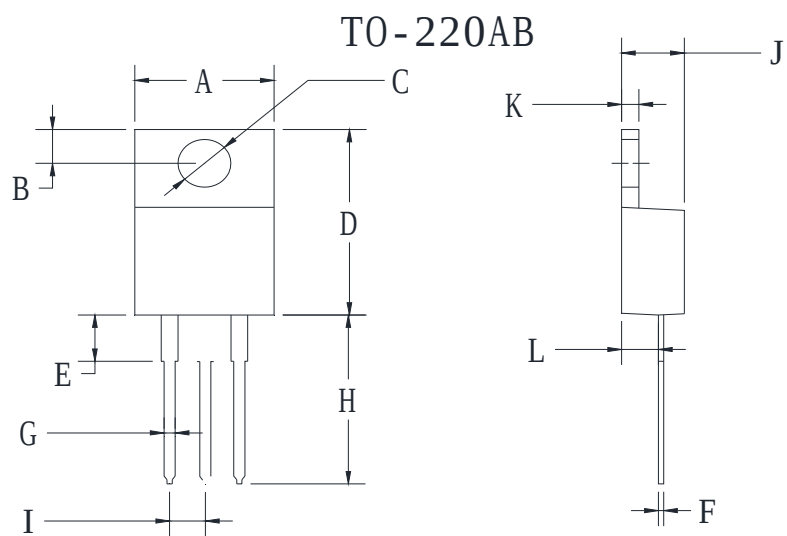
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